

WHAT IS CLAIMED IS:

1. A multiprocessing system comprising:
 - 5 a plurality of processing subsystems, each including a cache memory;
 - 10 a memory subsystem including a directory;
 - 15 a network interconnecting said plurality of processing subsystems and said memory subsystem;
wherein the network includes a mode unit configured to control whether a given coherency request is transmitted through said network according to a directory protocol or a broadcast protocol, and wherein said given coherency request is initiated by a requesting processing subsystem, and wherein an encoding of said given coherency request excludes an indication of whether said given coherence request is to be transmitted according to said directory protocol or said broadcast protocol.
- 15 2. The multiprocessing system as recited in Claim 1 wherein said directory includes a plurality of entries corresponding to different memory locations mapped to said memory subsystem, wherein each entry contains information indicative of whether a cached copy of a corresponding block has been created in one or more of said plurality of processing subsystems.
- 20 3. The multiprocessing system as recited in Claim 2 wherein when said given coherency request is transmitted through said network according to said broadcast protocol, said coherency request is broadcasted to said memory subsystem and to each of said plurality of processing subsystems regardless of information contained within said directory.
- 25 4. The multiprocessing system as recited in Claim 3 wherein said given coherency request is transmitted to said network through a point-to-point link.

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5. The multiprocessing system as recited in Claim 3 wherein when said coherency request is transmitted through said network according to said directory protocol, said given coherency request is transmitted through said network to said memory subsystem.

5 6. The multiprocessing system as recited in Claim 5 wherein when said given coherency request is transmitted through said network according to said directory protocol, said directory is accessed by said memory subsystem and responsive coherency commands are provided to one or more of said plurality of processing subsystems dependent upon information contained within said directory.

10 7. The multiprocessing system as recited in Claim 1 wherein said mode unit includes a coherency mode storage unit configured to store an indication to control whether said given coherency request is transmitted through said network according to said directory protocol or said broadcast protocol.

15 8. The multiprocessing system as recited in Claim 7 wherein said coherency mode storage unit is configured to store a plurality of additional indications to control whether other coherency requests are transmitted through said network according to said directory protocol or said broadcast protocol.

20 9. The multiprocessing system as recited in Claim 8 wherein selected coherency requests are transmitted through said network according to said directory protocol while other coherency requests are transmitted through said network according to said broadcast protocol.

25 10. The multiprocessing system as recited in Claim 9 wherein an address of each coherency request is used to dictate whether said each request is transmitted through said network according to said directory protocol or said broadcast protocol.

11. The multiprocessing system as recited in Claim 1 wherein said network is implemented using a plurality of address switches.

12. A multiprocessing system comprising:
5 a memory subsystem including a directory, the directory including a plurality of directory entries corresponding to memory locations mapped to said memory subsystem;
a plurality of processing subsystems each including a cache for storing blocks corresponding to selected ones of said memory locations;
10 a network interconnecting said plurality of processing subsystems and said memory subsystem;
a mode unit configured to provide an indication of whether a given block is a point-to-point mode block or a broadcast mode block;
wherein the network is configured to route a coherency request for said given
15 block initiated by one of said processing subsystems to said directory in response to said indication indicating said given block is a point-to-point mode block, and wherein said network is configured to broadcast said coherency request to said memory subsystem and to each of said plurality of processing subsystems in response to said indication indicating said given block is a broadcast mode block, whereby a conveyance of said coherency request through said network according to either a directory protocol or a broadcast protocol is performed transparent to said one of said processing subsystems.

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25 13. A method of operating a multiprocessing system including a plurality of processing subsystems and a memory subsystem interconnected through a network, the method comprising:
30 a first processing subsystem initiating a coherency request; and

a mode unit indicating whether said coherency request is to be transmitted through said network according to a directory protocol or a broadcast protocol;

5 said network conveying said coherency request using either said directory protocol or said broadcast protocol according to said indication and transparent to said first processing subsystem.

14. The method as recited in claim 13 further comprising said mode unit accessing a 10 coherency mode storage unit that stores an indication that indicates whether said coherency request is to be transmitted through said network according to said directory protocol or said broadcast protocol.

15. The method as recited in claim 13 further comprising accessing a directory entry 15 in response to said coherency request when said coherency request is transmitted through said network according to said directory protocol.

16. A multiprocessing system comprising:

20 a plurality of processing subsystems;

 a memory subsystem;

25 a network interconnecting said plurality of processing subsystems and said memory subsystem; and

30 a mode unit configured to control whether a coherency request is transmitted through said network according to a directory protocol or a broadcast protocol, whereby a conveyance of said coherency request through said network according to either said directory protocol or said broadcast

protocol is performed transparent to said plurality of processing subsystems.

17. The multiprocessing system as recited in claim 16 wherein said mode unit includes a coherency mode storage unit configured to store an indication to control whether said coherency request is transmitted through said network according to said directory protocol or said broadcast protocol.
18. The multiprocessing system as recited in claim 17 wherein said coherency mode storage unit is configured to store a plurality of additional indications to control whether other coherency requests are transmitted through said network according to said directory protocol or said broadcast protocol.
19. The multiprocessing system as recited in claim 18 wherein selected coherency requests are transmitted through said network according to said directory protocol while other coherency requests are transmitted through said network according to said broadcast protocol.
20. The multiprocessing system as recited in claim 19 wherein an address of each coherency request is used to dictate whether said each request is transmitted through said network according to said directory protocol or said broadcast protocol.

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